



## Trends and Challenges in Microelectronics and VLSI Design

## (6th September 2019)

Mini Colloquium held on 6<sup>th</sup> September'19. The student branch of IEEE IGDTUW along with IEEE-Electron Device Society and IEEE Delhi Chapter jointly hosted the Mini Colloquium on "Trends and Challenges in Microelectronics and VLSI Design" on 6th September'19 at the Seminar Hall, Admin Block, IGDTUW. The goal of the colloquium was to expose the interested students about the challenges and the scope of work in the field and give them insights beyond their existing academic knowledge. The event recorded around 300+ registrations from all across colleges in Delhi NCR and a footfall of 150+ at the event. The demographics was diverse with both post-graduates and graduates from wide-ranging courses being the active audience of the day.





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